

Ministry of Education and Science of Ukraine  
Ternopil Ivan Puluj National Technical University

Faculty of Applied Information Technology and Electrical Engineering

(full name of faculty)

Department of Biotechnical Systems

(full name of department)

# QUALIFYING PAPER

For the degree of

Bachelor

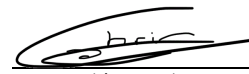
(degree name)

topic: Medical photocatalytic device for air disinfection and purification

Submitted by: fourth year student 4, group IRB-42

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Ministry of Education and Science of Ukraine  
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Faculty Faculty of Applied Information Technology and Electrical Engineering  
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Head of Department

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« »

2023

**ASSIGNMENT**  
**for QUALIFYING PAPER**

for the degree of

Bachelor

(degree name)

specialty

163 Biomedical engineering

(code and name of the specialty)

student

Pemba Ngoyi Christelle

(surname, name, patronymic)

1. Paper topic

Medical photocatalytic device for air disinfection and purification

Paper supervisor Dozorska O.F., PhD

(surname, name, patronymic, scientific degree, academic rank)

Approved by university order as of

2. Student's paper submission deadline

3. Initial data for the paper task for work,

4. Paper contents (list of issues to be developed)




## SUMMARY

Theme of qualification work: "Medical photocatalytic device for air disinfection and purification". Qualifying work of a bachelor // TNTU, ATF, group IRB-42. // Ternopil, 2023 // p.- , fig.- , table- , bibliog.- , appendix- .

Key words: photocatalytic device, air disinfection, printed circuit board.

In the qualification work, an overview of all stages of the life cycle of a medical photocatalytic device for disinfection and air purification was carried out. An analysis of the technical task was carried out, mathematical modeling of the operation of the medical photocatalytic device was carried out, a structural and functional scheme was built, an electrical diagram was built. The IR2156(S)PbF microcircuit was used as a PWM driver chip, and the parameters of the elements that set its operating modes were calculated.

The design section was completed, in which the selection of the element base was carried out, the design of the PCB, the printed unit was developed. When developing the device, the automated design system P-Cad 2006 was used. With the help of the P-Cad environment, the PCB was traced and the assembly drawing of the printed circuit was obtained.

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I am also grateful to my partner Josuñ Nsakala Ntsiama, for his editing help, late-night feedback sessions, and moral support. Thanks should also go to the librarians, research assistants, and study participants from the university, to my friends and colleagues especially Isaac Kofi Mamphey who supported, inspired and impacted me.

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## INTRODUCTION

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					PNCh 2.893.001				
			<i>Signature</i>	<i>Date</i>					
<i>Developed</i>	Pemba N. Ch.				Medical photocatalytic device for air disinfection and purification			<i>Page</i>	<i>Pages</i>
<i>Checked</i>	Dozorska O.F.								
					ATF, group IRB-42				





























as an electronic ballast implementation option. However, the first option is more common today.

To light a lamp, you need to heat its electrodes. Since there is no starter in the circuit of the electronic ballast, it is necessary to somehow first close the power circuit so that the flowing current heats up the electrodes, and then turn off the starting circuit.

Based on the structural diagram, we build the functional diagram shown in fig. 1.5.

The functional circuit includes the AC to DC input converter (block A1), the inverter power switches (blocks A2 and A3), the pulse-width modulation (PWM) driver (block A4), storage chokes (blocks A5 and A6) and the L1 and L2 lamps themselves.

On resistors R1-R4, a sensor for the state of the spiral of UV lamps is made. Resistors R3, R4 are connected to the power source. From lamp spirals, the opposite terminals of which are connected with a common wire. The other terminals of resistors R1, R2 are connected together and the signal from them is fed to the enable input of the PWM driver. This circuit works as follows: both pairs of resistors form voltage dividers. The resistances of the resistors must be large enough. However, the resistance of the spiral of UV lamps is much lower than the resistance of these resistors. When each UV lamp is working, the connection nodes of the pairs of resistors R1, R3 and R2, R4 are connected to a common wire. The signal at the output of dividers R1, R3 and R2, R4 is practically zero. The driver works fine. If the spiral of any lamp is broken, a high-amplitude signal will be generated at the output of the divider and will turn off the driver.



In the phase of energy transfer to the load, the voltage between the drain and the drain of the key transistors VT1, VT2 consists of the supply voltage and the current reaction voltage in the storage chokes L2, L3.

The amount of overvoltage can be twice the value of the supply voltage and even more. The voltage on the key transistors rises, albeit for a short time: a significant inductive emission appears. The stabilization circuit will, of course, track the change in load, i.e. reduce the duty factor or increase the conversion frequency. However, the response of the control scheme is never instantaneous, as it always has some inertia. It is fundamentally impossible to track short inductive emissions. In fig. 1.6 shows typical methods of protecting keys from overvoltage.

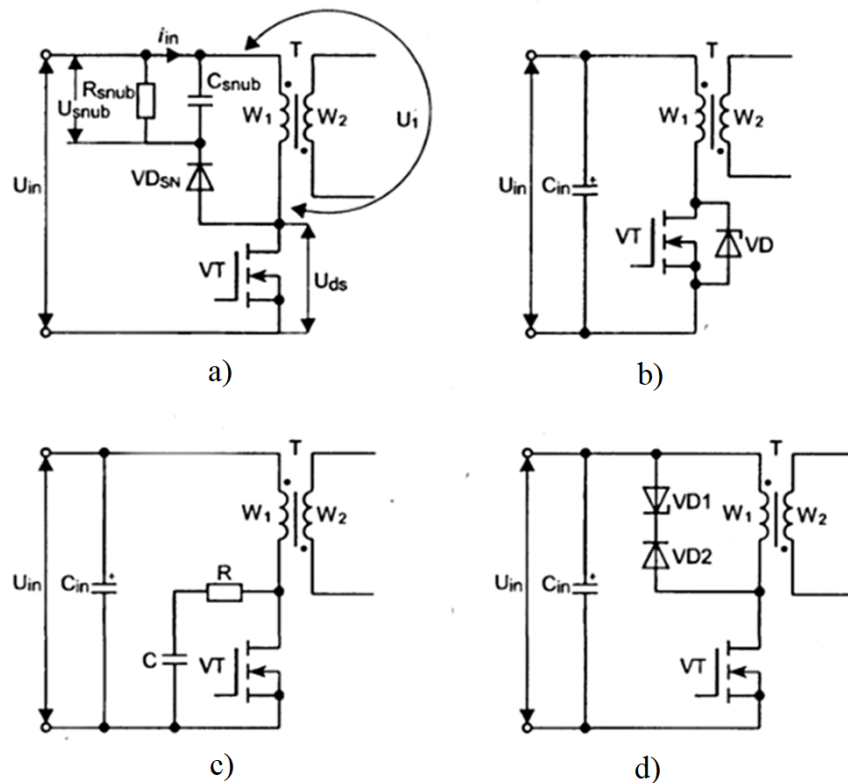


Figure 1.6 – Protection of a key transistor against a potential breakdown: a) locking link; b) the use of a suppressor (reverse-switched zener diode); c) a snubber (RC link) in the sink-leakage circuit; d) limiter of inductive emissions

The work uses power transistor switches that have an internal built-in protective diode - a suppressor.


### 1.2.5 Element calculations

The purpose of parametric synthesis is to calculate the nominal values of the elements of the electrical principle circuit. We will calculate the nominal values of the elements of individual circles of the circuit.

The VD1 diode bridge rectifies the mains voltage, so the reverse voltage of each diode must be greater than the amplitude value of the mains voltage, i.e. more than 308 V. We choose diodes of the DF10S type, for which the maximum reverse voltage is equal to 600 V, direct current –  $i_d = 2$  A, the maximum direct current in the pulse (with the duration of the pulse  $\tau_m < 10\text{ms}$ )  $i_{d.m} = 35$  A.

The capacity of the capacitor C1 is found by the formula:

$$C1 = \frac{P_n}{200 \cdot K_n \cdot U_s^2} \quad (1.3)$$

Here  $P_n$  – nominal power consumption (36 W),

$K_n$  – ripple coefficient,

$U_s$  – supply voltage (308 V),

$$C1 = \frac{36}{200 \cdot 0,1 \cdot 308^2} = 18,9(\mu F).$$

Let's take  $C1 = 22 \mu F$ .

The capacity of capacitor C2, which is a high-frequency filter, is assumed to be  $0.1 \mu F$ .

Varistor R2 is used to limit current consumption by lamps in case of failure of inductors or power transistor switches. In this case, the consumption current will begin to increase, the dissipation capacity of the varistor will begin to increase, its resistance will begin to increase and, accordingly, this will lead to a decrease in the



At the moment of start-up, the voltage on resistor will be 220 V. Then the dissipated power will be equal to:

$$P = U_{RI}^2 / R1 = 220^2 / 4,7 = 10297 \text{ (W)}.$$

However, such power is dissipated on the resistor less  $\tau_m < 10ms$ . The resistor itself does not have time to heat up. We take its power, taking into account the ambient temperature and power reserve, equal to 1 W.

We will calculate the PWM elements of the IR2156 driver.

The driver has the ability to set the value of the time delay between the edges of the pulses at the HO and LO outputs. It is set by capacitor C6 and determined from the formula:

$$t_{DT} = C6 \cdot 2000(s). \quad (1.6)$$

It is known that the switching time of field-effect transistors is less than 0.5  $\mu s$ , and the use of a time of about 10  $\mu s$  is practically not used in pulse technology. Therefore, we choose the delay time at the level of 1  $\mu s$ . Let's find the value of capacitor C6:

$$C6 = \frac{t_{DT}}{2000} = \frac{1 \cdot 10^{-6}}{2000} = 0.5 \cdot 10^{-9} \text{ (F)}.$$

Let's take the value of capacitor C6 equal to 470 pF.

The operating frequency of the PWM driver is set by capacitor C6 and resistor R4, and is determined by the formula:

$$f = \frac{1}{2 \cdot C6(0.51 \cdot R4 + 1475)} \text{ (Hz)}. \quad (1.7)$$





$$R8=R9=U/i_R=1/0,05=20 \text{ (Ohm)}.$$

Let's take the resistance of resistors R8, R9 equal to 22 Ohms.

Resistor R11 limits the current through the transistor switches and its resistance is determined from the condition that the current through it will be equal to 0.2 A (40 W, 220 V), and is found by the formula:

$$R6 = \frac{1,25}{I} = \frac{1,25}{0,2} = 6,25 \text{ (Ohm)}.$$

We take the resistance of resistor R6 equal to 5.6 ohms.

Accordingly, power will be dissipated on resistor R6:

$$P=U_{R6} \cdot I = I^2 \cdot R1 = 0,2^2 \cdot 5,6 = 0,28 \text{ (W)}. \quad (1.8)$$

We take its power, taking into account the ambient temperature and power reserve, equal to 1 W.

The nominal values of the remaining elements of the electrical principle scheme are calculated in a similar way.

### 1.3 Design of the device

#### 1.3.1 Selection of elements

The selection of the element base can be error-prone:

- rough, which lead to failure at the first start;
- errors that reduce the service life of the equipment.

Gross errors lead to losses and delays in setting up the equipment, but they are not the most dangerous because they are immediately visible.





- maximum operating frequency, MHz.....100;
- operating temperature range, °C..... -60 ... +85.

Transistor IRF830 - powerful transistors.

The main technical characteristics of the IRF830 transistor:

- maximum drain-leakage voltage, V.....400;
- maximum gate-drain voltage, V.....20;
- maximum current, A.....3,3;
- the maximum dissipated power of the collector, W.....50.

The selected elements will ensure the reliability and efficiency of the medical photocatalytic device for disinfection and air purification.

### 1.3.2 PCB development

Let's look at the design features of the device being developed. The most common today is the so-called SMT component installation technology (Surface Mounted Technology) (Fig. 1.7, b). However, the work uses the technology of mounting component leads in metallized holes (PTH - Plated Through Hole), which is shown in fig. 1.7, a.

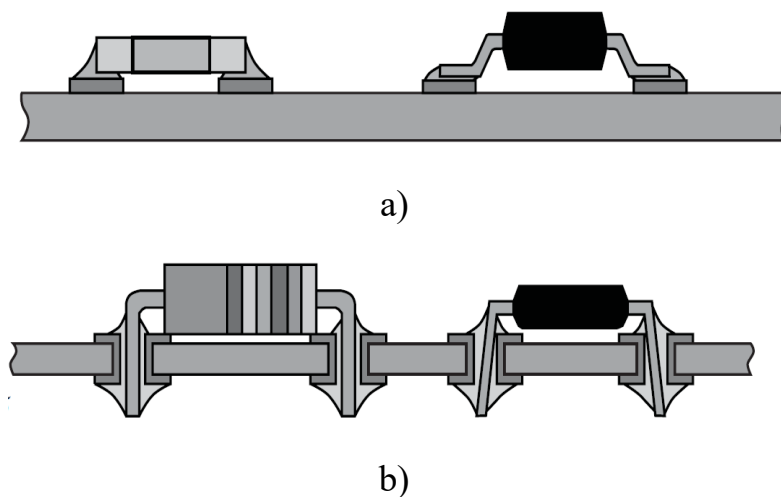


Figure 1.7 – Methods of installing elements on printed circuit boards: a – surface mounting (SMT mounting); b - installation of terminals in holes

Production of electronic modules faces the question of choosing the optimal configuration of the technological line: assembly modules (installation of electronic





The name of the main parameter of the board pattern		The value of the main parameter of the drawing in the bottleneck for the board of the accuracy class			
		2	3	4	5
The width of the printed conductor	The nominal value on the drawing, not less	0,55	0,35	0,23	0,15
	Limit deviation	+0,10 -0,15	+0,05 -0,15	+0,05 -0,10	±0,04
	The minimum permissible value (for the option of setting the dimensions of the non-conductive pattern)	0,4	0,2	0,13	0,11
The distance between the elements of the leading pattern	The minimum permissible value on the drawing	0,4	0,2	0,13	0,10
	Minimum value (for trace calculation)	0,5	0,325 0,275	0,235 0,27	0,21 0,16
The width of the element of the non-conductive pattern (for the option)	Nominal value on the drawing, no less	0,5	0,275	0,235	0,16
setting the dimensions of a non-conductive drawing	Limit deviation	+0,15 -0,10	+0,125 -0,075	+0,10 -0,05	±0,06
Warranty belt of the contact pad for a non-metallized hole	Nominal value for calculating the diameter of the contact pad	0,5	0,45	0,4	0,35
Contact pad warranty belt for metallized hole	Nominal value for calculating the diameter of the contact pad	0,45	0,35	0,25	0,25
Contact pad diameter	Nominal value on the drawing, no less	Table 1.3, table 1.4			
	Limit deviation	+0,10 -0,15	+0,05 -0,15	+0,05 -0,10	±0,04












0.25 mm - for class 5 boards.

The cross-sectional area of the conductive track  $S$  is found from the expression:

$$S = \frac{i}{j} \quad (1.9)$$

where:  $i$  is the maximum current that should flow through the conductor,  $j$  is the current density for the conductor material (for copper, we take  $j=5 \text{ A/mm}^2$ ).

The maximum current will flow through the power transistor switches and will be:

$$i = \frac{P}{U}, \quad (1.10)$$

where  $U$  is the supply voltage of the block,  $P$  is the power consumption of one lamp.

$$i = \frac{P}{U} = \frac{20}{220} = 0.091 \text{ A}$$

Then  $S$  is equal to the product of the thickness  $p$  of the conductive material (50  $\mu\text{m}$ ) by the width of the conductor  $l$ . Let's find the minimum width of the conductor:

$$S = p \cdot l = \frac{i}{j}, \quad (1.11)$$

$$l = \frac{i}{j \cdot p} = \frac{0,091}{5 \cdot 50 \cdot 10^{-3}} = 0,37 \text{ mm}$$

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printed unit is attached to the plastic case, which has racks with holes, using self-tapping screws and washers.

The recommended annual production program is 5,000 units.

#### 1.4.2 Determination of the type of production

Modern production is divided into types: single, serial and mass.

The type of production is characterized by the coefficient of seriality:

$$K_c = \frac{t_{\epsilon}}{T_{art}} = \frac{F_d \cdot 60}{T_{art} \cdot N} \quad (1.12)$$

The coefficient of seriality determines the number of different operations for the manufacture of a part or product fixed at one workplace during the year.

The following values of the coefficient of seriality are accepted:

- for mass production  $K_c = 1$
- for large-scale production  $K_c = 2 - 10$
- for medium-scale production  $K_c = 10 - 20$
- for small-scale production  $K_c > 20$

For unit production, the serialization factor is not regulated.

where  $t_B$  - output tact:

$$t_p = \frac{F_d \cdot 60}{N}; \quad t_p = \frac{2070 \cdot 60}{5000} = 24,8 \text{ min}, \quad (1.13)$$

where  $N$  - annual production program ( $N = 5000$  units/year);

$F_d$  - for one shift 2070 min/unit, for two shifts - 4140.

$T_{art}$  - artificial time ( $T_{sht}$  is approximately equal to 3 minutes).

$$K_c = \frac{24,8}{3} = 8,3$$























## CONCLUSIONS

In the qualification work, an overview of all stages of the life cycle of a medical photocatalytic device for disinfection and air purification was carried out. In the first section of the work, an analysis of the technical task was carried out, the purpose of which is to clarify the requirements set by the customer, mathematical modeling of the operation of the medical photocatalytic device for disinfection and air purification was carried out, a structural and functional scheme was built, which is a logical continuation of the structural scheme and is built on the basis of the last . Using the functional scheme, an electrical principle diagram was built and, accordingly, a parametric synthesis was carried out, the essence of which is to calculate the parameters of the elements of the electrical principle diagram.

The IR2156(S)PbF microcircuit was used as a PWM driver chip, and the parameters of the elements that set its operating modes were calculated.

The design section was completed, in which the selection of the element base was carried out, the design of the PCB, the printed unit was developed, the calculations of mechanical loads and thermal calculations were carried out. When developing the device, the automated design system P-Cad 2006 was used. With the help of the P-Cad environment, the PCB was traced and the assembly drawing of the printed circuit was obtained.

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# APPLICATIONS


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*Apk*





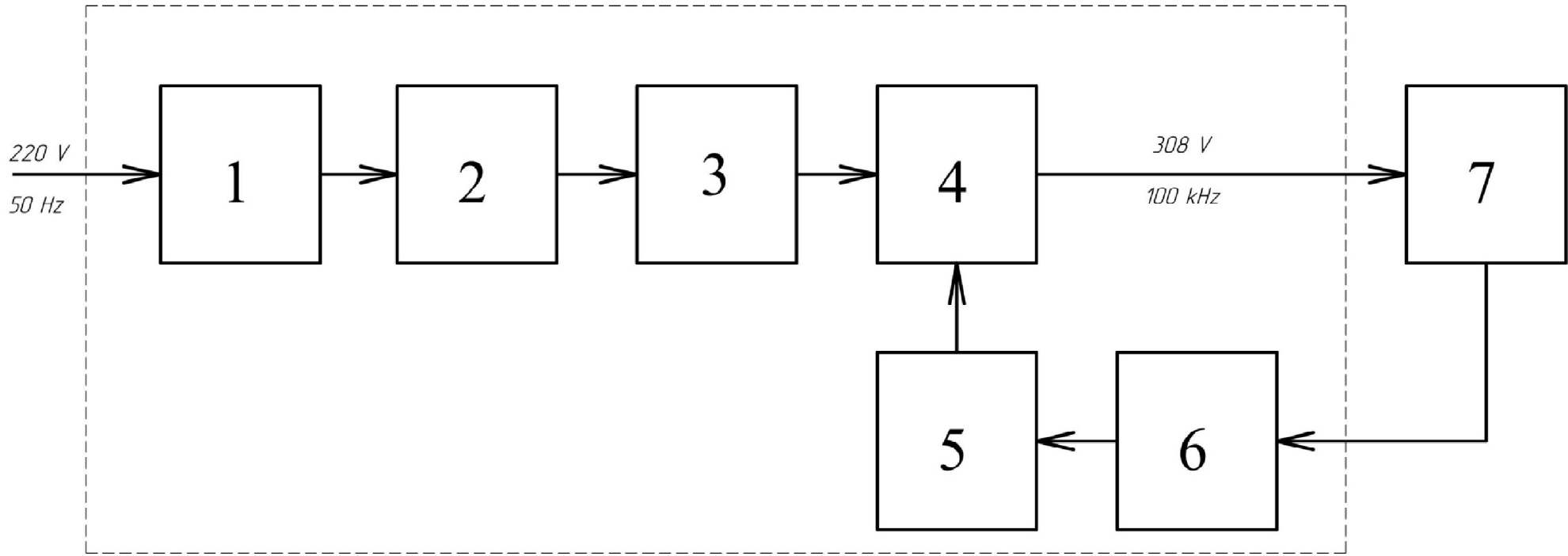






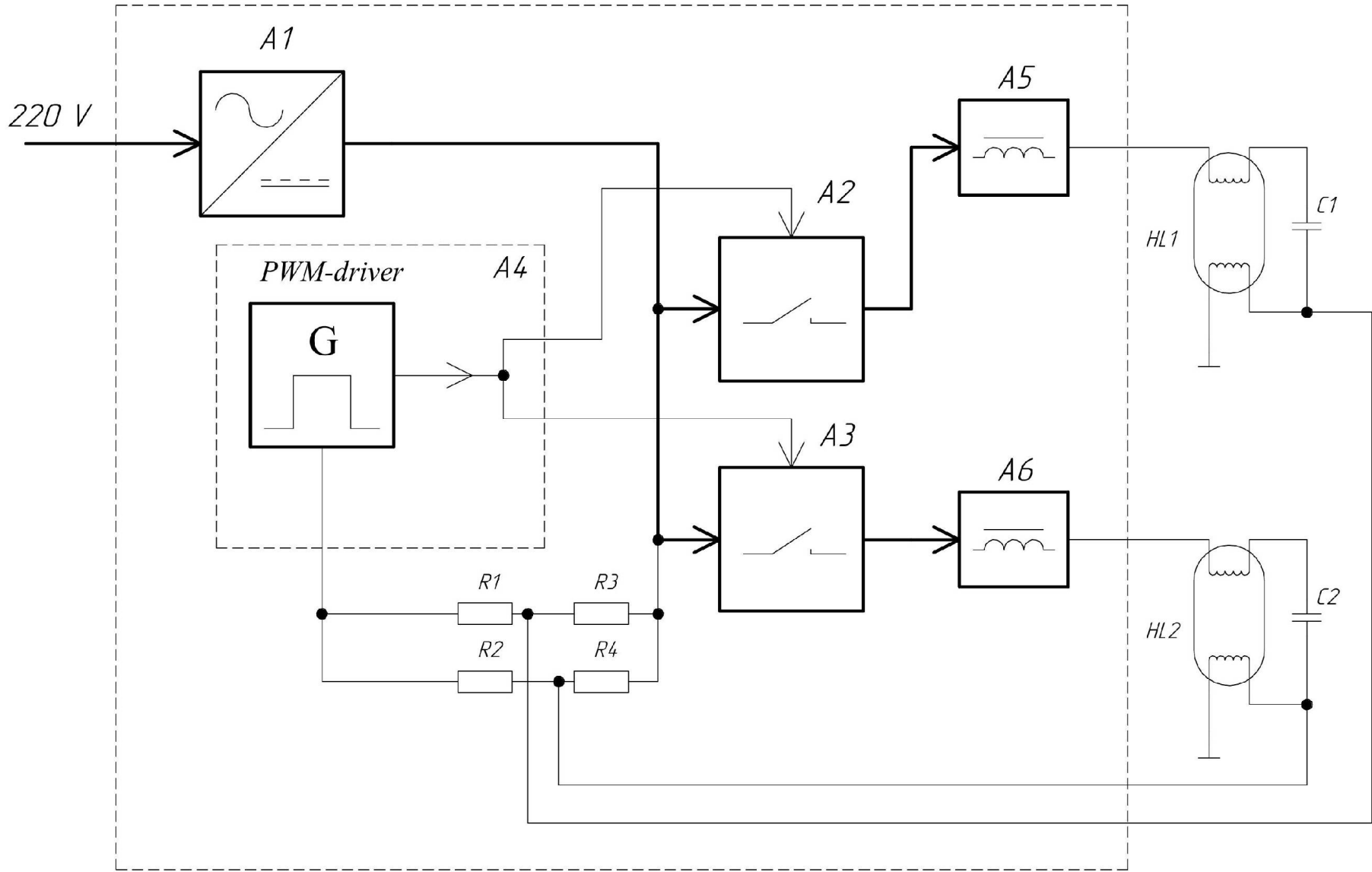


PNCh 2.893.001 E1

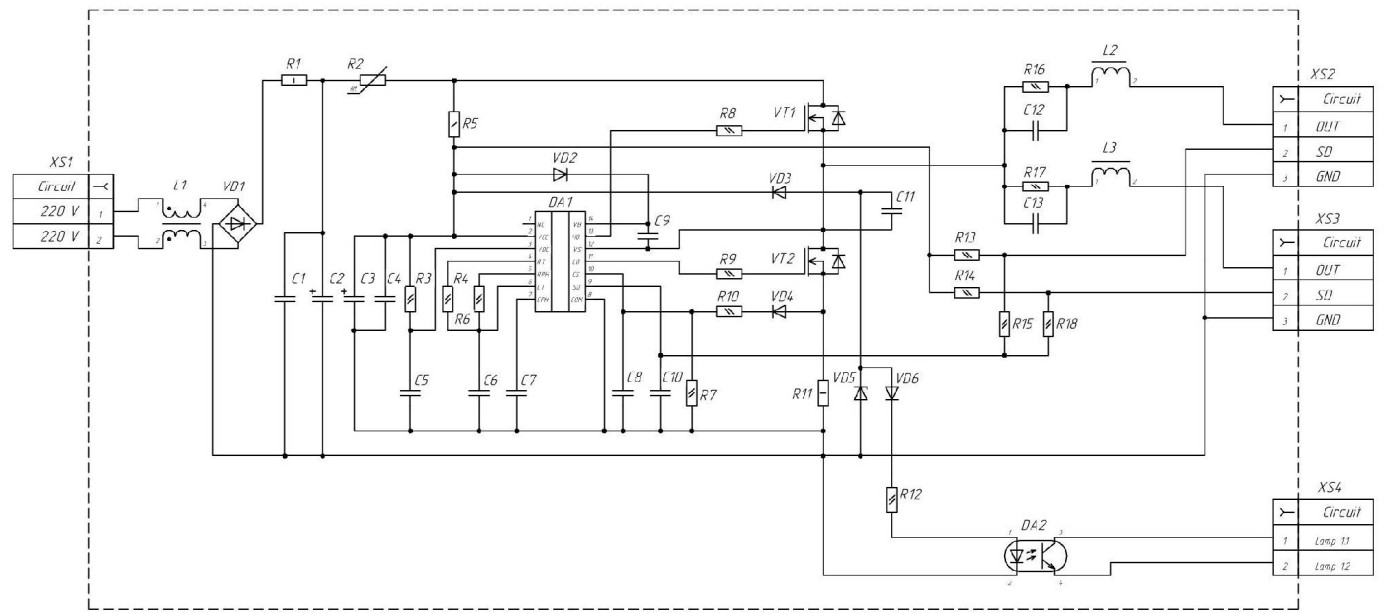


- 1 - Anti-interference filter
- 2 - Rectifier
- 3 - Filter
- 4 - Switch
- 5 - Pulse-width modulation (PWM) driver
- 6 - Feedback loops
- 7 - Ultraviolet lamp

		PNCh 2.893.001 E1	
		Medical photocatalytic device for air disinfection and purification Structural diagram	
Developed	Pemba NCh	- -	
Checked	Ouzarska OP		
Head of Dept.	Yavorska EB		
		ATF, group IRB-42	



		PNCh 2.893.001 E2	
		Medical photocatalytic device for air disinfection and purification Functional diagram	
Developed	Pemba N.Lh.		
Checked	Dazorska O.F.		
Rec'd of Desig.	Yavorska E.B.		
		ATF, group IRB-42	



		PNCh 2.893.001 E3			
		Medical photocoagulation device for air disinfection and purification. Electric design.			
Developed	Pravko A.D.				
Checked	Galazovska D.F.				
Model type	Televrska Z.D.				
				ATF, group IRB-4.2	

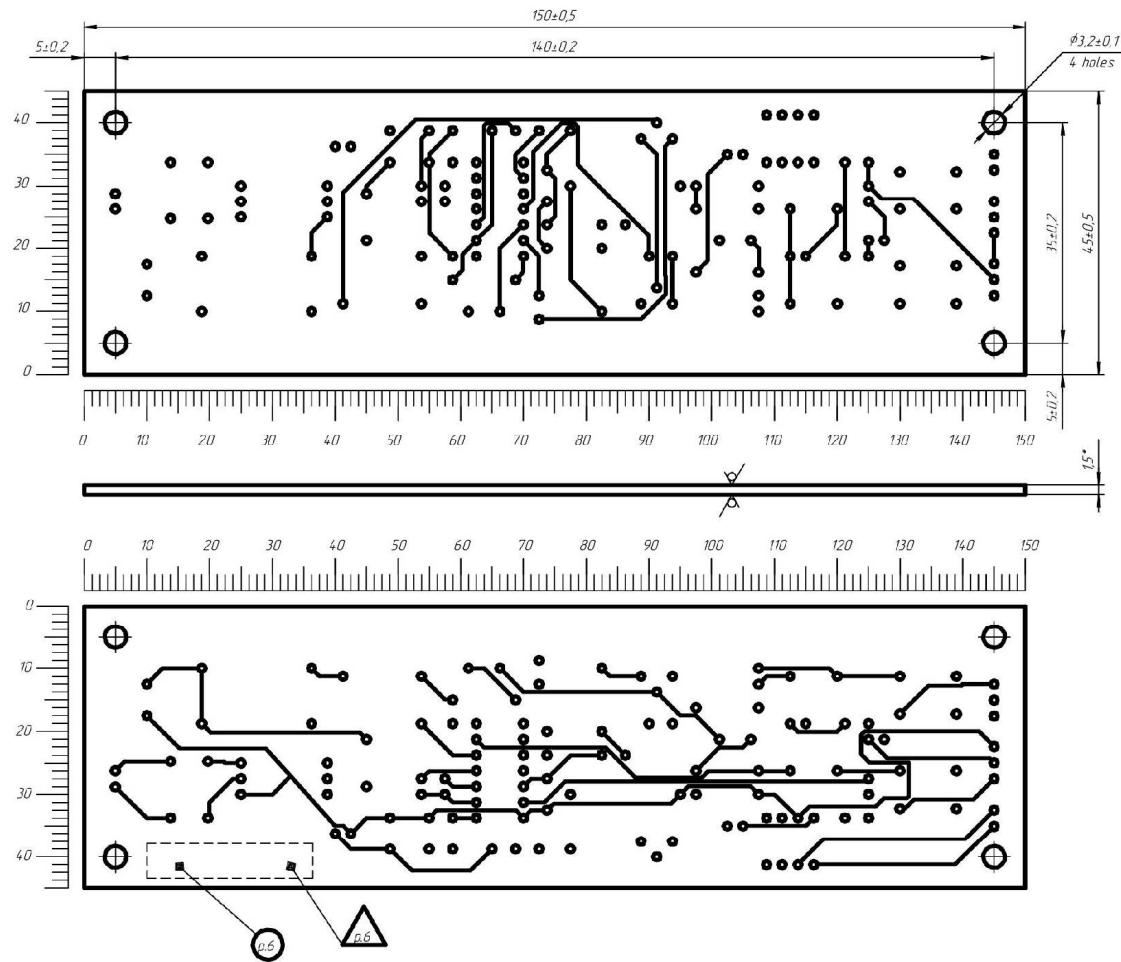
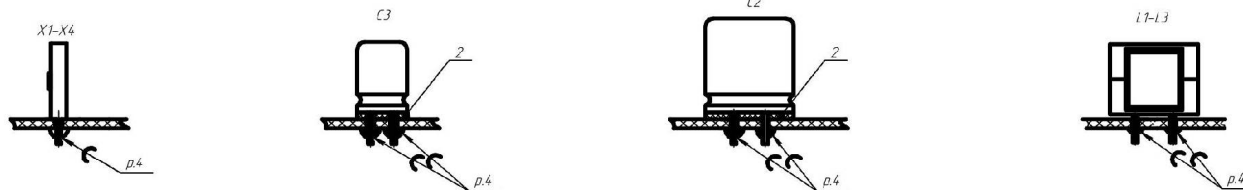
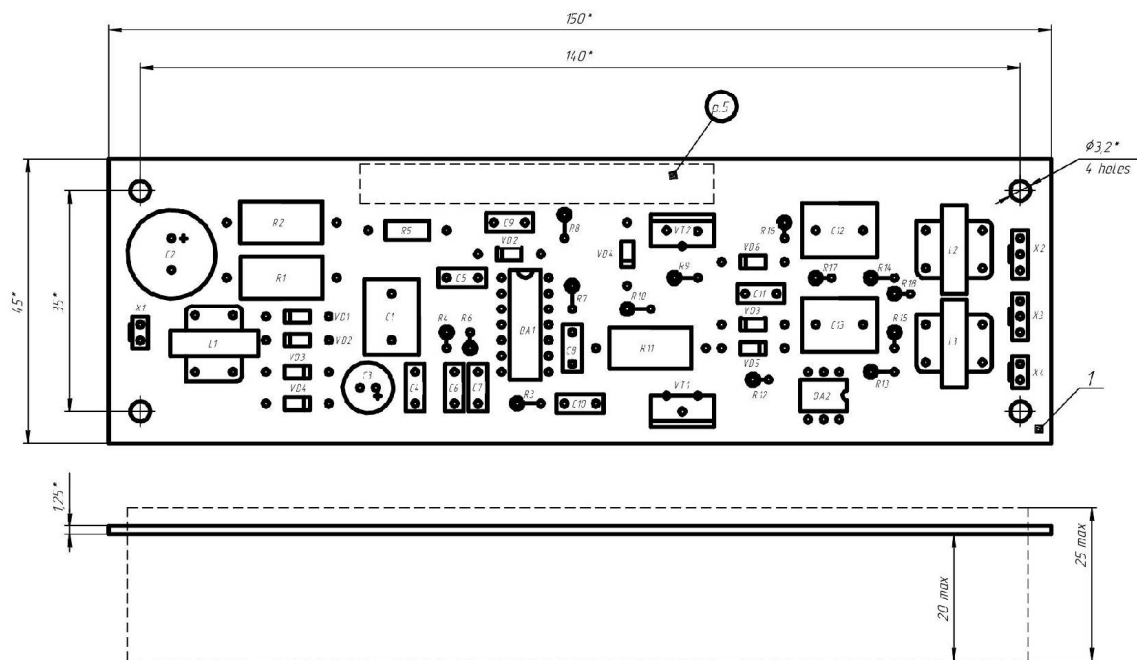


Table 1

Designation of holes	Hole diameter, mm	The size of the contact pads	Presence of metalization	Number
⊕	3,2±0,1	-	-	4
⊙	0,8±0,05	17±0,05	+	128

1. \* Size for reference.
2. The board must meet the requirements of current standards, rigidity group 2, accuracy class 3. The pitch of the coordinate grid is 1.25 mm.
3. Make the payment in a combined positive way.
4. Configuration of printed conductors according to the drawing.
5. Mark the factory number with black marking paint.

				PNCh 7.102.001	
				Printed board	
Version	2.5.1	PNCh	7.102.001		
Author	Tavarsko EB	Designer	Tavarsko EB		
				ATF, group IRB-42	



- 1 \*Sizes for reference.
2. Prepare for installation in accordance with current standards
3. Install the elements as follows. Elements C1, C4-C13 set according to option Ia, resistors R1, R2, R5, R11 and diodes - according to option Ia, resistors R3, R4, R6-R10, R12-R18 - according to option IIa, V11, V12 - according to option IV, DA1, DA2 - according to option VIIa, other elements - according to the drawing. The step of the coordinate grid is 1.25 mm.
4. Solder with solder.
5. Mark the number, date, letter of the change - font 25 with black marking point.
6. Elements with a schematic designation are shown conditionally.

		PNCh 3.893.001	
		Circuit board	
Developed	Wojciech Klich		
Checked	Wojciech Klich		
Approved	Wojciech Klich		
		ATF, group IRB-42	